

SPI

```
diff --git a/arch/arm/boot/dts/i2c-b6ull.dts b/arch/arm/boot/dts/i2c-b6ull.dts
--- a/arch/arm/boot/dts/i2c-b6ull.dts
+++ b/arch/arm/boot/dts/i2c-b6ull.dts
@@ -101,14 +101,14 @@
         interrupt-parent = <&gpio5>;
         interrupts = <2 IRQ_TYPE_EDGE_FALLING>;
         spi-max-frequency = <10000000>;
-        status = "okay";
+        status = "disabled";
     };
};
```